

What is Claimed is:

1. A liquid crystal display device comprising:  
a liquid crystal display element;  
plural driving circuits;  
a display control device which transmits display data  
and a clock signal to the plural driving circuits; and  
a circuit board which is provided between the display  
control device and the plural driving circuits and supplies  
the display data and the clock signal transmitted from the  
display control device, to each of the driving circuits via  
a bus line and a clock line in the circuit board,

each of the bus line and the clock signal line of the  
circuit board being formed in a continuous area of the circuit  
board and being divided into plural lines.

2. A liquid crystal display device according to claim  
1, wherein the display control device supplies the display  
data and the clock signal to each of the divided bus lines  
and clock signal lines in sequence in accordance with  
transmission timing.

3. A liquid crystal display device according to claim  
2, wherein the display control device supplies a signal of  
fixed voltage level to each of the divided bus lines and clock  
signal lines to which the display data and the clock signal  
are not supplied.

4. A liquid crystal display device according to claim

1, wherein each of the bus line and the clock signal line of the circuit board is divided into two lines.

5. A liquid crystal display device according to claim 4, wherein the display control device sequentially supplies the display data and the clock signal to one of the bus lines and one of the clock signal lines and to the other of the bus lines and the other of the clock signal lines in sequence in accordance with transmission timing.

6. A liquid crystal display device according to claim 5, wherein while the display control device is supplying the display data and the clock signal to one of the bus lines and one of the clock signal lines, the display control device supplies signals of fixed voltage level to the other of the bus lines and the other of the clock signal lines.

7. A liquid crystal display device according to claim 4, wherein a connector for inputting the display data and the clock signal from the display control device is provided in a lengthwise central portion of the circuit board.

8. A liquid crystal display device according to claim 1, wherein the clock signal is a clock signal for latching display data.

9. A liquid crystal display device comprising:  
a liquid crystal display element;  
plural driving circuits;  
a display control device which transmits display data

and a clock signal to the plural driving circuits; and

a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line in the circuit board,

each of the bus line and the clock signal line of the circuit board being formed in a continuous area of the circuit board and being divided into plural lines, and a connector for inputting the display data and the clock signal from the display control device being provided in a portion other than a lengthwise end portion of the circuit board.

10. A liquid crystal display device according to claim 9, wherein the connector is provided in a lengthwise central portion of the circuit board.

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